PATENT ABSTRACTS OF JAPAN

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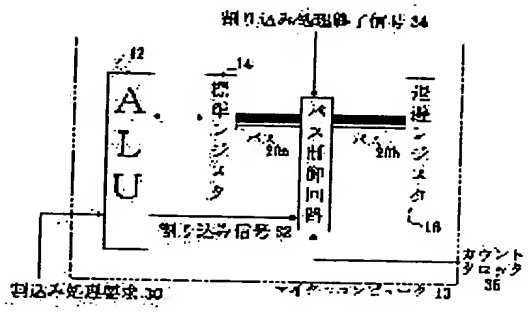
(54) MICROCOMPUTER

(57)Abstract:
PROBLEM TO BE SOLVED: To transit to interrupting processing at high a speed at the time of interruption occurrence.
SOLUTION: A saving register 16 to be used for holding the same data as data held in a standard register 14 is provided in addition to the standard register 12 to be used for an ALU 12 at the time of ordinary operation, data in the standard register 16 flow to the saving register 16 by a bus control circuit 18 at the time of ordinary operation, data held in the standard register 14 flow to the saving register 16 and when an interruption occurs, the flow of data from the standard register 14 to the saving register 16 is stopped. When the interruption processing is finished, the flow of data between the standard register 14 and saving register 16 is controlled so as to let the data held in the saving register 16 flow to the standard register 14 and to hold them.



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